



Pan European Training Platform & International HPC Summer Schools

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HPC SPECTRA



Main Objectives

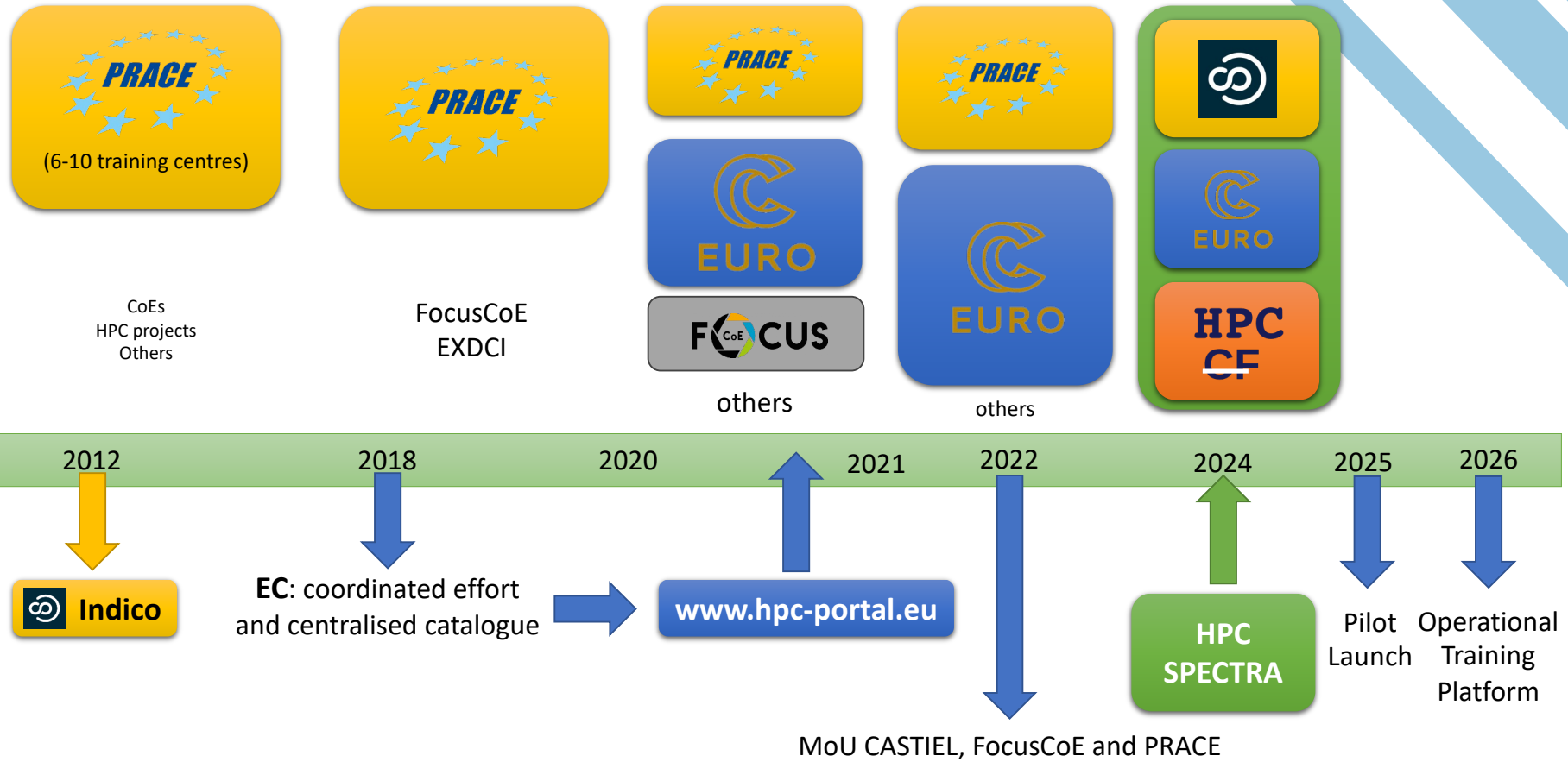
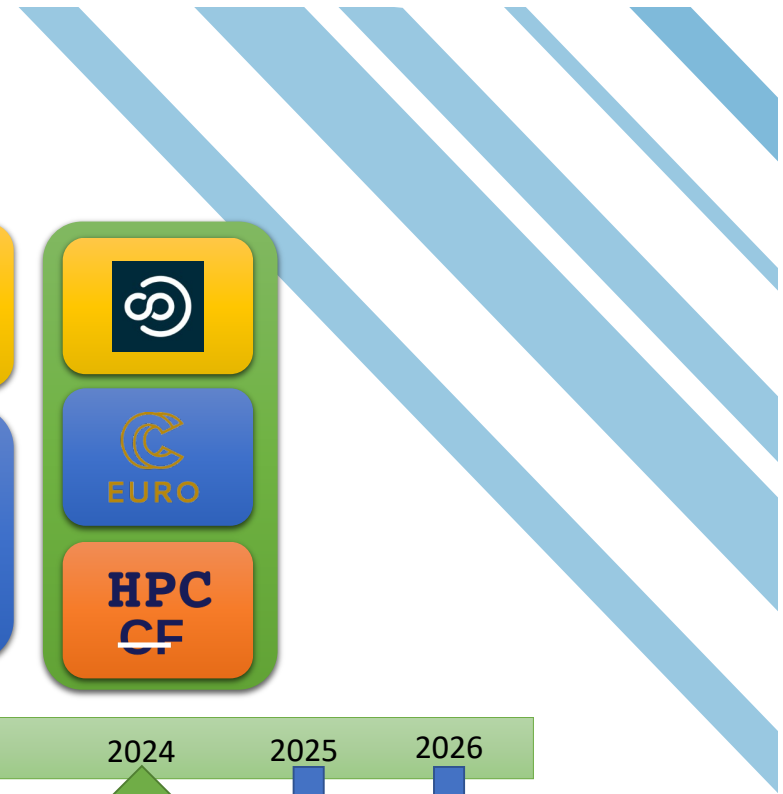


Pan-European HPC Training Platform

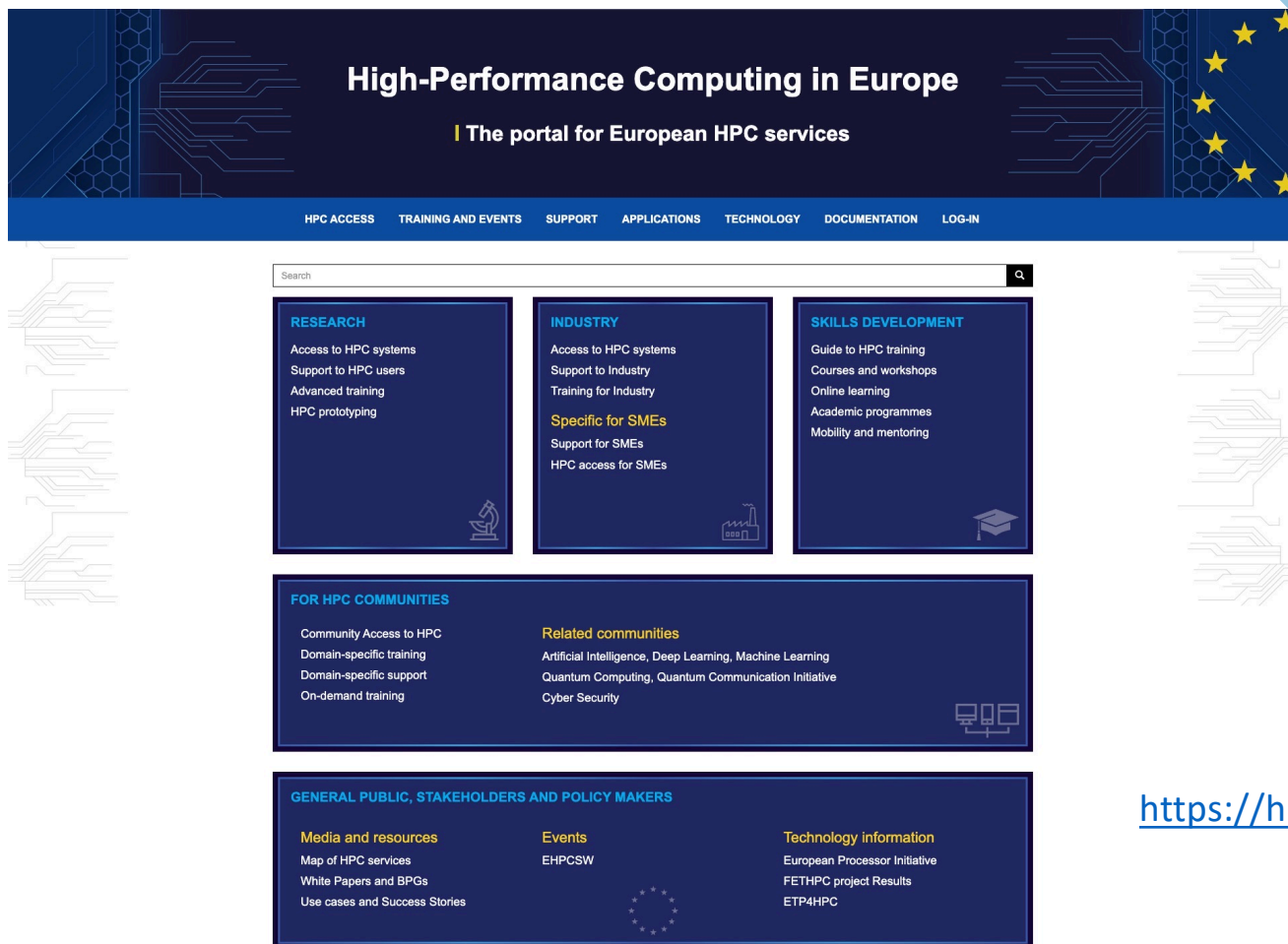


International HPC Summer Schools 2024 & 2025

Previous Activities



HPC in Europe Portal



The screenshot shows the homepage of the HPC in Europe Portal. At the top, there is a dark blue header with the text "High-Performance Computing in Europe" and "The portal for European HPC services". Below this is a navigation menu with links for "HPC ACCESS", "TRAINING AND EVENTS", "SUPPORT", "APPLICATIONS", "TECHNOLOGY", "DOCUMENTATION", and "LOG-IN". A search bar is located below the navigation menu. The main content area is divided into three columns: "RESEARCH", "INDUSTRY", and "SKILLS DEVELOPMENT". Below these columns is a section for "FOR HPC COMMUNITIES" and a final section for "GENERAL PUBLIC, STAKEHOLDERS AND POLICY MAKERS". The page is decorated with circuit-like patterns and the European Union flag.

High-Performance Computing in Europe
| The portal for European HPC services

HPC ACCESS TRAINING AND EVENTS SUPPORT APPLICATIONS TECHNOLOGY DOCUMENTATION LOG-IN

Search

RESEARCH
Access to HPC systems
Support to HPC users
Advanced training
HPC prototyping

INDUSTRY
Access to HPC systems
Support to Industry
Training for Industry
Specific for SMEs
Support for SMEs
HPC access for SMEs

SKILLS DEVELOPMENT
Guide to HPC training
Courses and workshops
Online learning
Academic programmes
Mobility and mentoring

FOR HPC COMMUNITIES
Community Access to HPC
Domain-specific training
Domain-specific support
On-demand training

Related communities
Artificial Intelligence, Deep Learning, Machine Learning
Quantum Computing, Quantum Communication Initiative
Cyber Security

GENERAL PUBLIC, STAKEHOLDERS AND POLICY MAKERS

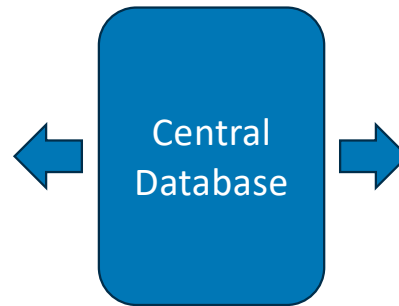
Media and resources
Map of HPC services
White Papers and BPGs
Use cases and Success Stories

Events
EHPCSW

Technology information
European Processor Initiative
FETHPC project Results
ETP4HPC

<https://hpc-portal.eu/>

Database -> Different Displays



Aims and Target Audiences



Improve overall look and feel



Better user experience – finding relevant courses, support for skills and learning pathways



Support European HPC providers – adding course information and metadata, support for registrations, evaluation and materials (regardless of project/institution affiliation)



Facilitate HPC stakeholders – overview of training landscape in Europe

Current Activities

Integration of C2ISS with Indico (event management system).

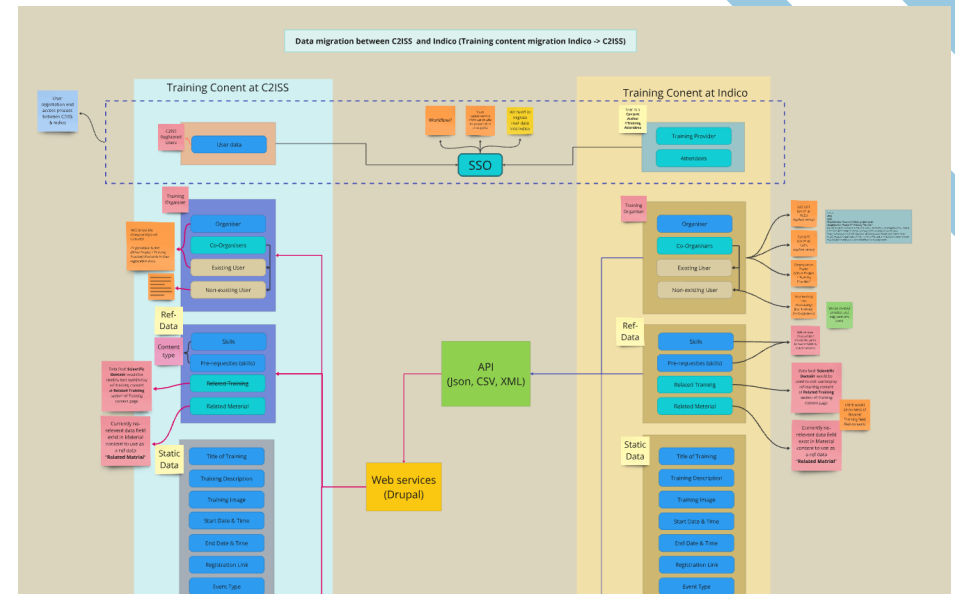
- Indico: gather training event information
- C2ISS: display of all/filtered list of courses

Associating courses with skills trees

Interface to learning pathways that are being developed by CASTIEL 2 and future EuroHPC projects

Repository of training materials

(Optional) Event management tools for training providers, e.g. registration, feedback



Incorporation of Skills/Competence Framework

HPC CF

Home About Competencies Certificates Participate Ecosystem Processes Roadmap Downloads

The HPC Certification Forum

The goals of the HPC Certification Forum (HPC-CF) are:

- Standardizing the HPC knowledge representation into fine-grained competencies
- Establishing internationally recognized certificates attesting knowledge
- Supporting an ecosystem around the HPC competencies

The HPC Certification Forum is one of those ideas so good and so right that you wonder why no one thought of it before. The Forum's Steering Board and members have done the technical community an enormous service by providing a concise training in what it takes to be HPC-literate. I wish we had had this thirty years ago.

- John Gustafson, Chief Technology Officer at Ceranovo, inventor of Gustafson's Law in parallel computing

- The HPC Certification Forum
- News
- Contributing Institutions
- Contact Us

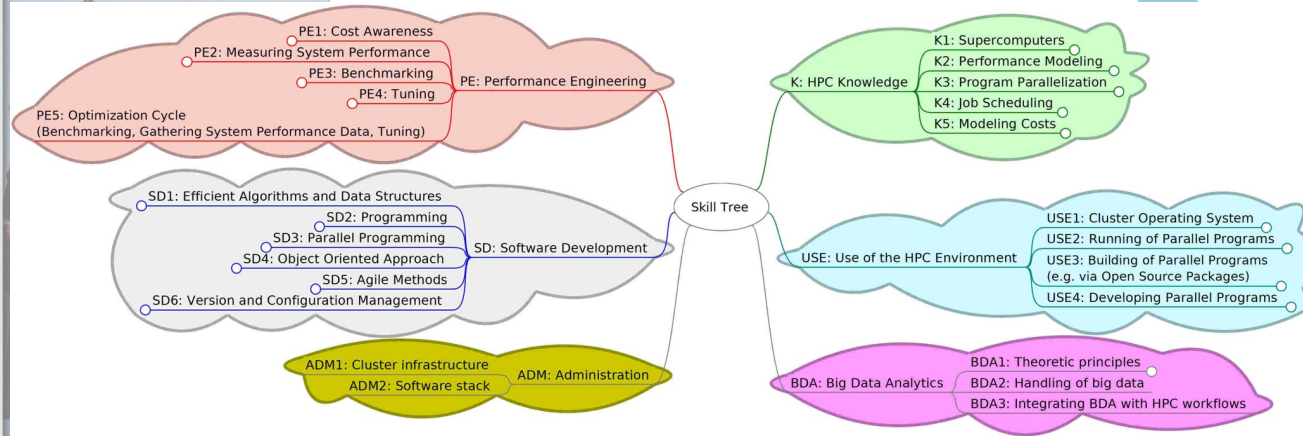
Video: Towards a Globally Acknowledged and Free HPC Certification: Classification of Competences == Skills

- A skill defines background, objectives, learning outcomes
- The skill tree organizes the competences as hierarchical skills
- Certificates bundle several skills into attestable unit

Figure: Top-levels of the skill tree (Initial ADM and BDA branches)

News

2023-11-13: Birds-of-a-feather at Supercomputing
A summary of the BoF and the slides are now available



Timeline

End 2024: Development & deployment of portal in collaboration with C2ISS

Early 2025: Pilot phase of functional portal

Mid 2025: Full launch of portal, usage monitoring, sustainability

Thank you!

This project has received funding from the European High-Performance Computing Joint Undertaking (JU) under grant agreement No 101136267. The JU receives support from the European Union's Digital Europe Programme.



Funded by
the European Union



EuroHPC
Joint Undertaking